



CORNERSTONE Device Dimensions for 340 nm SOI Platform

Grating etch depth = 140 nm





Changes from previous version

- Single mode strip waveguide default width increased to 450 nm
- 1x2 strip MMI added





TE mode
 $\lambda = 1550 \text{ nm}$

Grating etch depth = 140 nm

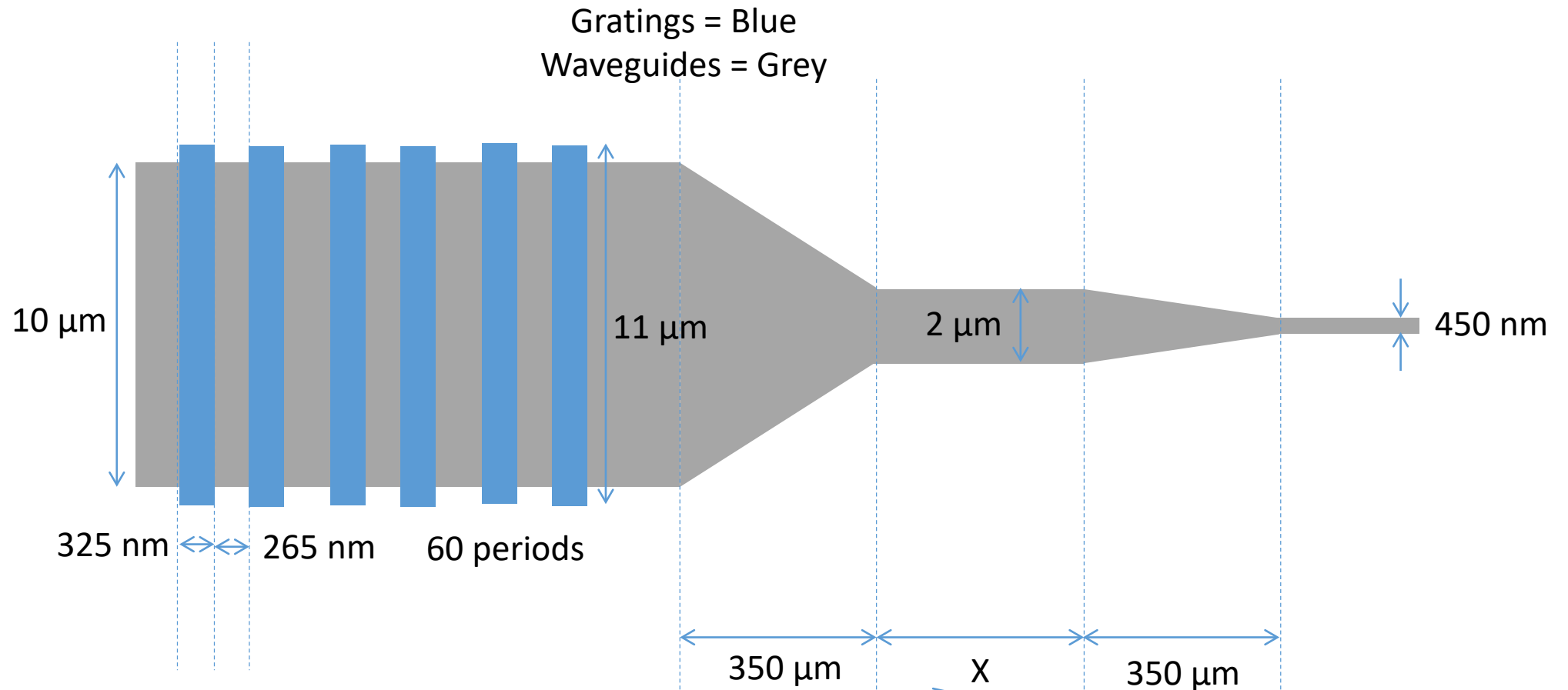




TE single mode waveguides and bends

Property	Wavelength = 1550 nm
<u>Strip waveguides</u>	
Maximum single mode waveguide width (nm)	450
Minimum bend radius (width = 450 nm) (μm)	10

TE grating coupler (etch depth = 140 nm)



Use waveguide of width X for low loss routing to devices if required

TE 1x2 STRIP MMI dimensions

*Prototype design that has not yet been tested

